

APPLICANT(S): LEBONHEUR, Vassoudevane  
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#### AMENDMENTS TO THE CLAIMS

Please add or amend the claims to read as follows, and cancel without prejudice or disclaimer to resubmission in a divisional or continuation application claims indicated as cancelled:

1. (Currently Amended) A device comprising:  
a first electrical unit;  
a second electrical unit; and  
a first set of electrical connections extending from the second electrical unit, each of the first set of electrical connections including a distal tip and a base, wherein at least a distal portion of each of the first set of electrical connection narrows in a linear fashion towards the distal tip, wherein each of the first set of electrical connections including at least 50% copper, has a melting point which is above 400 degrees Celsius.
2. (Original) The device of claim 1, wherein the first electrical unit is a substrate and the second electrical unit is a semiconductor die.
3. The device of claim 1, wherein the first electrical unit is a semiconductor die.
4. (Original) The device of claim 1, wherein each of the first set of electrical connections includes at least 80% copper.
5. (Original) The device of claim 1, wherein each of the first set of electrical connections has a melting point of at least 400 degrees Celsius.
6. (Original) The device of claim 1, wherein each of the first set of electrical connections is tapered.
7. (Currently Amended) The device of claim 1, wherein along a portion of the first set of electrical connections the width of the electrical connections increases with the distance from the ~~die~~ second electrical unit
8. (Original) The device of claim 1, wherein each of the first set of electrical connections has a triangular or substantially triangular side cross section.
9. (Original) The device of claim 1, wherein each of the first set of electrical connections has a conical or substantially conical shape.

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10. (Original) The device of claim 1, wherein each of the first set of electrical connections has a pentagonal or substantially pentagonal side cross section.
11. (Original) The device of claim 1, wherein each of the first set of electrical connections has a shape of a frustum or has a substantially frustum-like shape.
12. (Original) The device of claim 1, comprising a non-conductive material disposed between the first electrical unit and the second electrical unit.
13. (Original) The device of claim 1, comprising a first set of electrical connections extending from the first electrical unit and connecting with the second set of connections.
14. (Currently Amended) A device comprising:
  - a substrate including substrate electrical connections; and
  - a semiconductor die including set of die electrical connections, wherein each of the die electrical connections has a melting point which is above 400 degrees Celsius, each of the die electrical connections having a first end connection area and a second end connection area, the first end connection area being connected to the semiconductor die, the first end connection area being wider than the second end connection [[area.]] area, wherein the die electrical connections narrow from the first end connection area to the second end connection area in a linear fashion.
15. (Original) The device of claim 14, wherein each of the die electrical connections includes at least 50% copper.
16. (Original) The device of claim 14, wherein each of the die electrical connections has a triangular or substantially triangular side cross section.
17. (Original) The device of claim 14, wherein each of the die electrical connections has a conical or substantially conical shape.
18. (Original) The device of claim 14, comprising a non-conductive material disposed between the semiconductor die and the substrate.
19. (Currently Amended) A device comprising:
  - a substrate;
  - a semiconductor die; and
  - a set of die electrical connections extending from the semiconductor die, wherein each of the die electrical connections has a melting point which is above 400 degrees

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Celsius, each of the die electrical connections including a distal tip and a base, wherein the distal tip is narrower than a portion closer to the base, wherein each of the die electrical connections includes at least 50% copper the die electrical connections narrow from the base to the distal tip in a linear fashion.

- 20 (Original) The device of claim 19 wherein each of the die electrical connections includes at least 80% copper.
- 21 (Original) The device of claim 19, wherein each of the die electrical connections has a melting point of at least 400 degrees Celsius.
- 22 (Original) The device of claim 19, wherein each of the die electrical connections is tapered.
- 23 (Original) The device of claim 19, wherein each of the die electrical connections has a triangular or substantially triangular side cross section.
- 24 (Original) The device of claim 19, wherein each of the die electrical connections has the shape of a frustum or has a substantially frustum-like shape.
- 25 (Original) The device of claim 19, comprising a non-conductive material disposed between the semiconductor die and the substrate.
- 26 (Original) The device of claim 19, comprising substrate electrical connections extending from the substrate and connecting with the die electrical connections.
- 27 (Currently Amended) A device comprising:
  - a processor, the processor including:
    - a substrate;
    - a semiconductor die; and
    - a set of electrical connections extending from the semiconductor die, wherein each of the die electrical connections has a melting point which is above 400 degrees Celsius, each of the electrical connections including a distal tip and a base, wherein the distal tip is narrower than the base, wherein each of the electrical connections includes at least 50% copper; the die electrical connections narrow from the base to the distal tip in a linear fashion; and
  - a DRAM.
- 28 (Original) The device of claim 27, wherein each of the electrical connections includes at least 80% copper.

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29. (Original) The device of claim 27, comprising a non-conductive material disposed between the semiconductor die and the substrate.

30. (New) A package assembly method comprising:

pressing a die comprising electrical connections and a substrate comprising electrical connections together, wherein the die electrical connections narrow in a linear fashion towards the end furthest from the die and have a melting point which is above 400 degrees Celsius;

inserting a filler material between the die electrical connections and the substrate electrical connections; and

connecting the die electrical connections to the substrate electrical connections.

31. (New) The method of claim 30, wherein each of the die electrical connections has a flat tip.

32. (New) The method of claim 30, wherein the die electrical connections include at least 50% copper.